



**Tilecal ADC: Quality Control Summary**

Serial Number	PS2017
Start Date	10-Oct-00
End Date	11-Oct-00
QC Run	PS2017Y2000M10D11H11M43S57.dat

**Visual Inspection**

	Comment	Result
PCB flatness		OK
PCB finishing		OK
Top Parts Positioning		OK
Bottom Parts Positioning		OK
Top Parts Soldering		OK
Bottom Parts Soldering		OK
Contact Pins		OK

**Automatic Tests**

	Comment	Result
Identifier Allocation		OK
Initialization (ISP and FB)		OK
Reprogramming (FA and FB)		OK
Serial Number Access		OK
Control Parameters		OK
3in1 Control Interface		OK
PMT List Control		OK
Single Conversions		OK
Trigger		OK
Autoscan		OK
Reset		OK

**Calibration Data**

	Comment	Result
Offset/DAC Count	mV/Count	-3.99
Offset Centerpoint	DAC Count	127.73
ADC Transfer Gain	Counts/mV	1.0585

**Comments**

PLD FAILURE: CANNOT LOAD A NEW PLD CODE VERSION. BOARD CANNOT INIT AND PLD MUST BE REPLAC

**Certification**

	Comment	Result
QC Result	PLD FAILURE	BAD
Signed by		BLANCHOT
Date		9-Nov-00